



Store.M

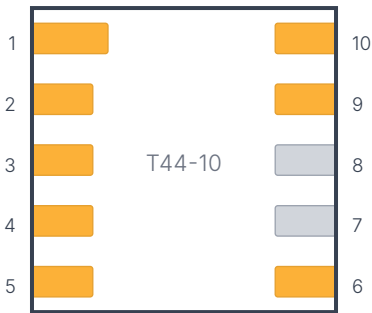
128Mbit flash storage

The Store.M contains 128Mbits of NOR flash memory for non-volatile code and data storage with support for single, dual, or quad SPI communications at bus-clock rates of up to 104MHz.

Overview

Revision	a
Package	T44-10
Supply	1.71-2V
Component	AT25QL128A
Interfaces	SPI, QSPI

Pad Assignments



(top view)

PAD	TYPE	FUNCTION	NOTE
1	power	GND	
2	interface	SPI.MOSI	
	interface	QSPI.IO0	
3	interface	SPI.CS	
	interface	QSPI.CS	
4	interface	SPI.CLK	
	interface	QSPI.CLK	
5	interface	SPI.MISO	
	interface	QSPI.IO1	
6	digital	WP	
	interface	QSPI.IO2	
9	digital	HOLD	
	interface	QSPI.IO3	
10	power	V+	1.71-2.0V



Interfaces

SPI

Mode

Max Clock

slave

104MHz

FUNCTION	REQ	PAD(S)
SPI.MOSI	Yes	2
SPI.CS	Yes	3
SPI.CLK	Yes	4
SPI.MISO	Yes	5

QSPI

Mode

Max Clock

slave

104MHz

FUNCTION	REQ	PAD(S)
QSPI.IO0	No	2
QSPI.CS	No	3
QSPI.CLK	No	4
QSPI.IO1	No	5
QSPI.IO2	No	6
QSPI.IO3	No	9

Application Notes